

Advance Information
SWITCHMODE™
Schottky Power Rectifier
DPAK Power Surface Mount Package

... employing the Schottky Barrier principle in a large area metal-to-silicon power diode. State of the art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies, free wheeling diode and polarity protection diodes.

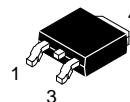
- Highly Stable Oxide Passivated Junction
- Guardring for Stress Protection
- Matched dual die construction – May be Paralleled for High Current Output
- High dv/dt Capability
- Short Heat Sink Tap Manufactured – Not Sheared
- Very Low Forward Voltage Drop
- Epoxy Meets UL94, VO at 1/8"

Mechanical Characteristics:

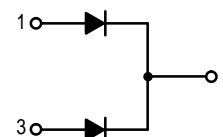
- Case: Epoxy, Molded
- Weight: 0.4 gram (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes:
260°C Max. for 10 Seconds
- Shipped in 75 units per plastic tube
- Available in 16 mm Tape and Reel, 2500 units per Reel,
Add "T4" to Suffix part #
- Marking: B1035CL

MBRD1035CTL

**SCHOTTKY BARRIER
RECTIFIER
10 AMPERES
35 VOLTS**



**CASE 369A-13
DPAK**



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	35	Volts
Average Rectified Forward Current (At Rated V_R , $T_C = 115^\circ\text{C}$)	Per Leg I_O Per Package	5 10	Amps
Peak Repetitive Forward Current (At Rated V_R , Square Wave, 20 kHz, $T_C = 115^\circ\text{C}$)	Per Leg I_{FRM}	10	Amps
Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	Per Package I_{FSM}	50	Amps
Storage / Operating Case Temperature	T_{stg}, T_C	-55 to +125	$^\circ\text{C}$
Operating Junction Temperature	T_J	-55 to +125	$^\circ\text{C}$
Voltage Rate of Change (Rated V_R , $T_J = 25^\circ\text{C}$)	dv/dt	10,000	V/ μs

THERMAL CHARACTERISTICS

Thermal Resistance – Junction to Case	Per Leg	$R_{\theta JC}$	2.43	$^\circ\text{C/W}$
Thermal Resistance – Junction to Ambient (1)	Per Leg	$R_{\theta JA}$	68	$^\circ\text{C/W}$

(1) Rating applies when using minimum pad size, FR4 PC Board

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This document contains information on a new product. Specifications and information herein are subject to change without notice.

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ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage ⁽²⁾ , see Figure 2 $I_F = 5 \text{ Amps}, T_J = 25^\circ\text{C}$ $I_F = 5 \text{ Amps}, T_J = 100^\circ\text{C}$ $I_F = 10 \text{ Amps}, T_J = 25^\circ\text{C}$ $I_F = 10 \text{ Amps}, T_J = 100^\circ\text{C}$	Per Leg	V_F	0.47 0.41 0.56 0.55	Volts
Maximum Instantaneous Reverse Current, see Figure 4 $(V_R = 35 \text{ V}, T_J = 25^\circ\text{C})$ $(V_R = 35 \text{ V}, T_J = 100^\circ\text{C})$ $(V_R = 17.5 \text{ V}, T_J = 25^\circ\text{C})$ $(V_R = 17.5 \text{ V}, T_J = 100^\circ\text{C})$	Per Leg	I_R	2.0 30 0.20 5.0	mA

(2) Pulse Test: Pulse Width $\leq 250 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

TYPICAL CHARACTERISTICS

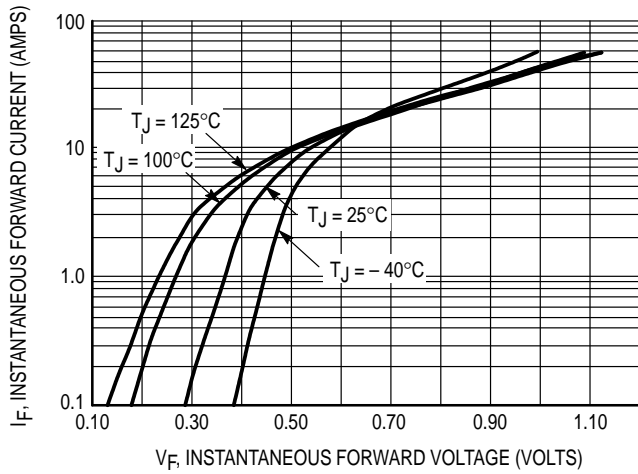


Figure 1. Typical Forward Voltage Per Leg

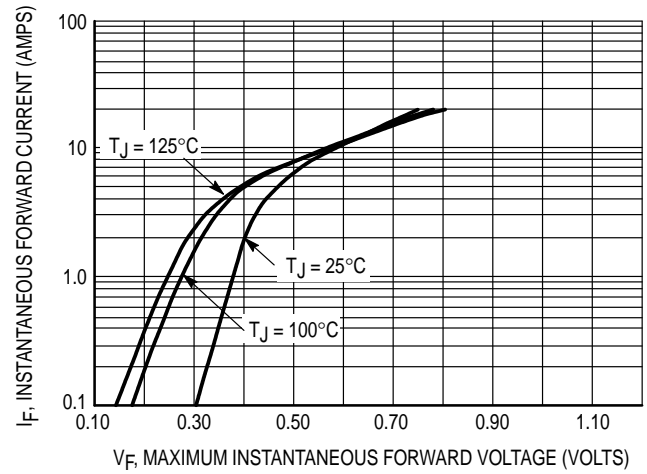


Figure 2. Maximum Forward Voltage Per Leg

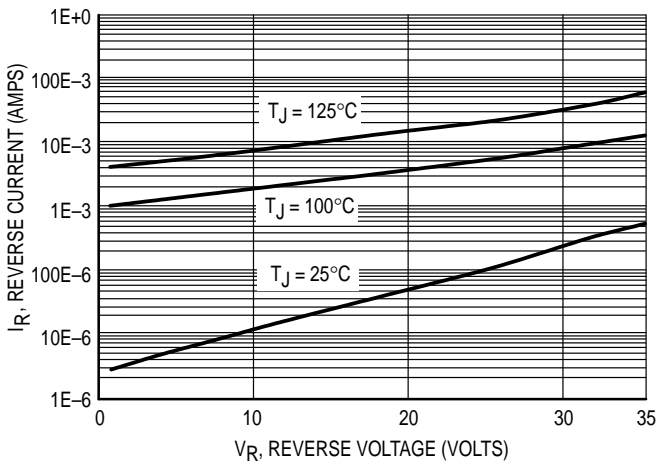


Figure 3. Typical Reverse Current Per Leg

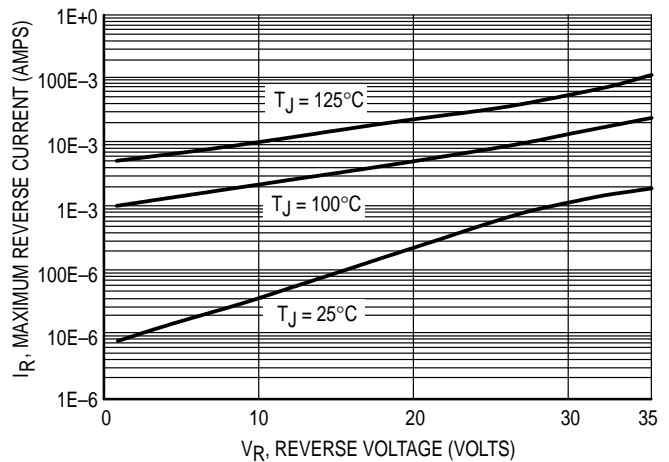


Figure 4. Maximum Reverse Current Per Leg

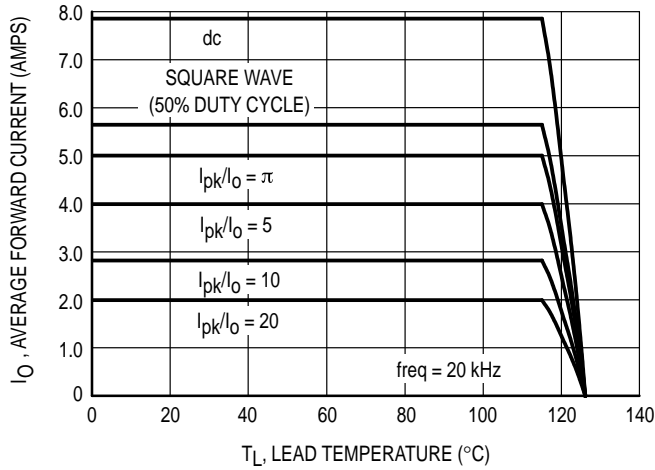


Figure 5. Current Derating Per Leg

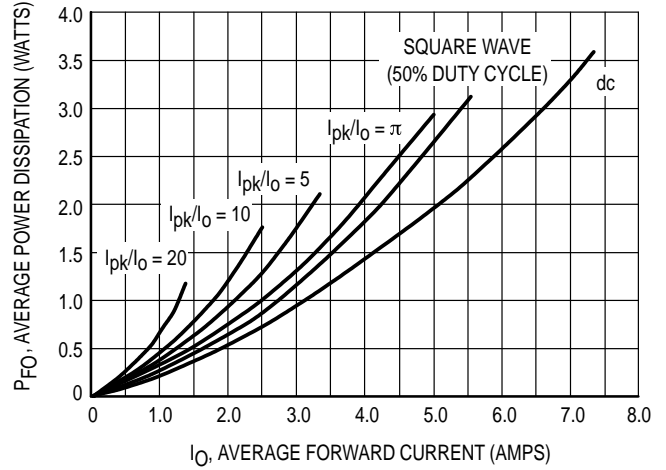


Figure 6. Forward Power Dissipation Per Leg

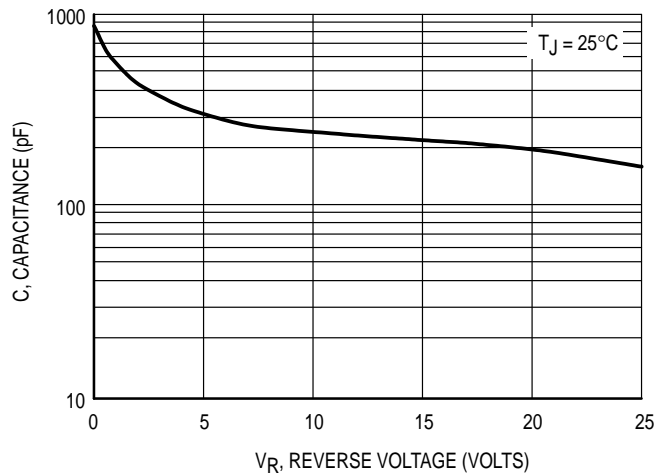


Figure 7. Capacitance Per Leg

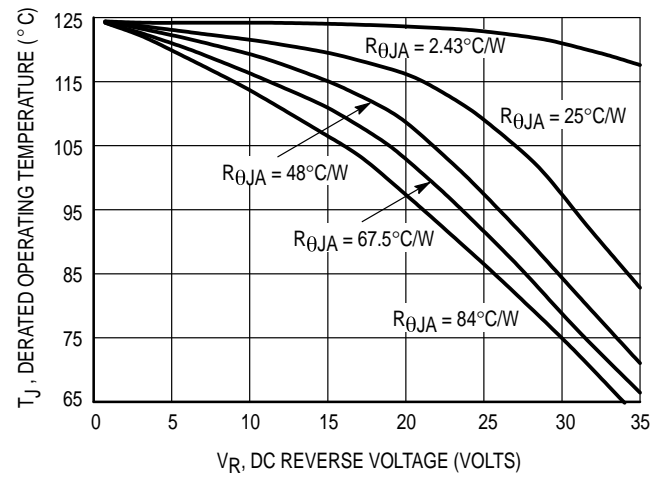


Figure 8. Typical Operating Temperature Derating Per Leg *

* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation:

$$T_J = T_{Jmax} - r(t)(P_f + P_r) \text{ where}$$

$r(t)$ = thermal impedance under given conditions,
 P_f = forward power dissipation, and
 P_r = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)P_r$, where $r(t) = R_{thja}$. For other power applications further calculations must be performed.

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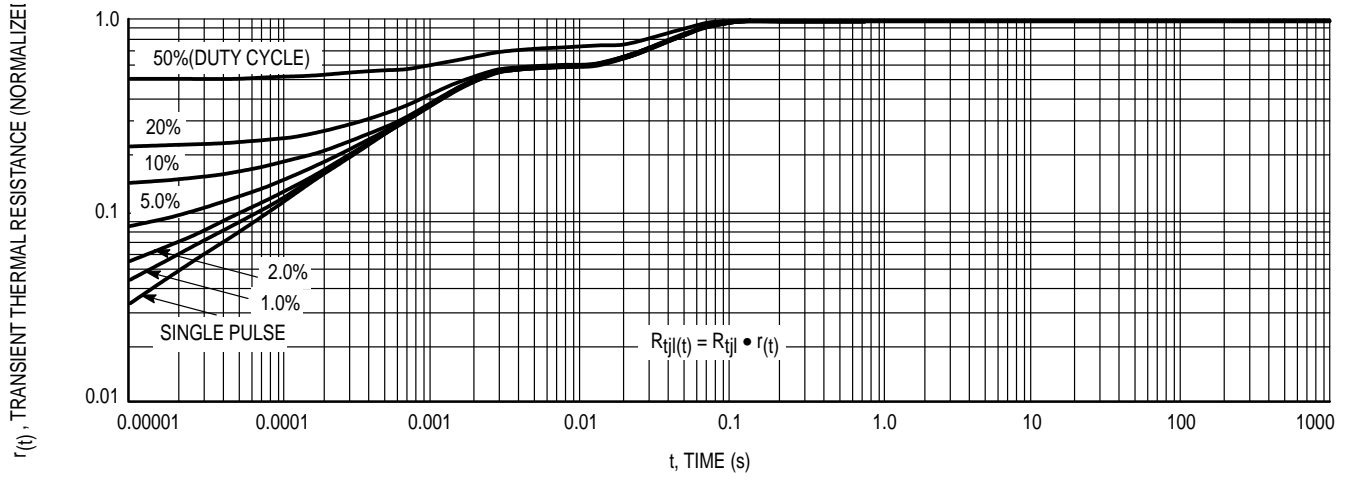


Figure 9. Thermal Response Junction to Case (Per Leg)

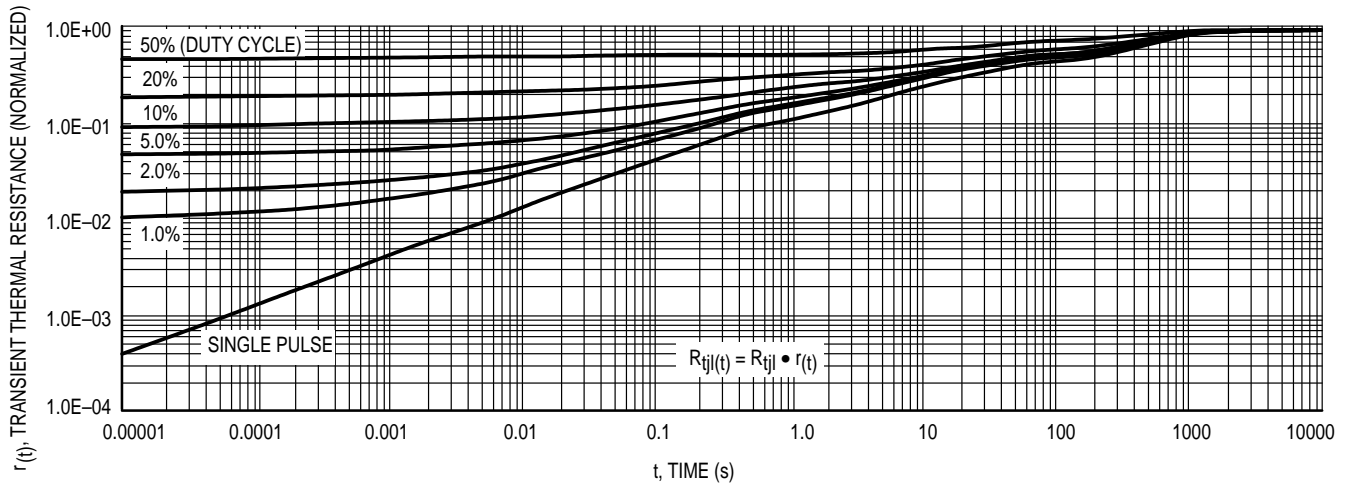
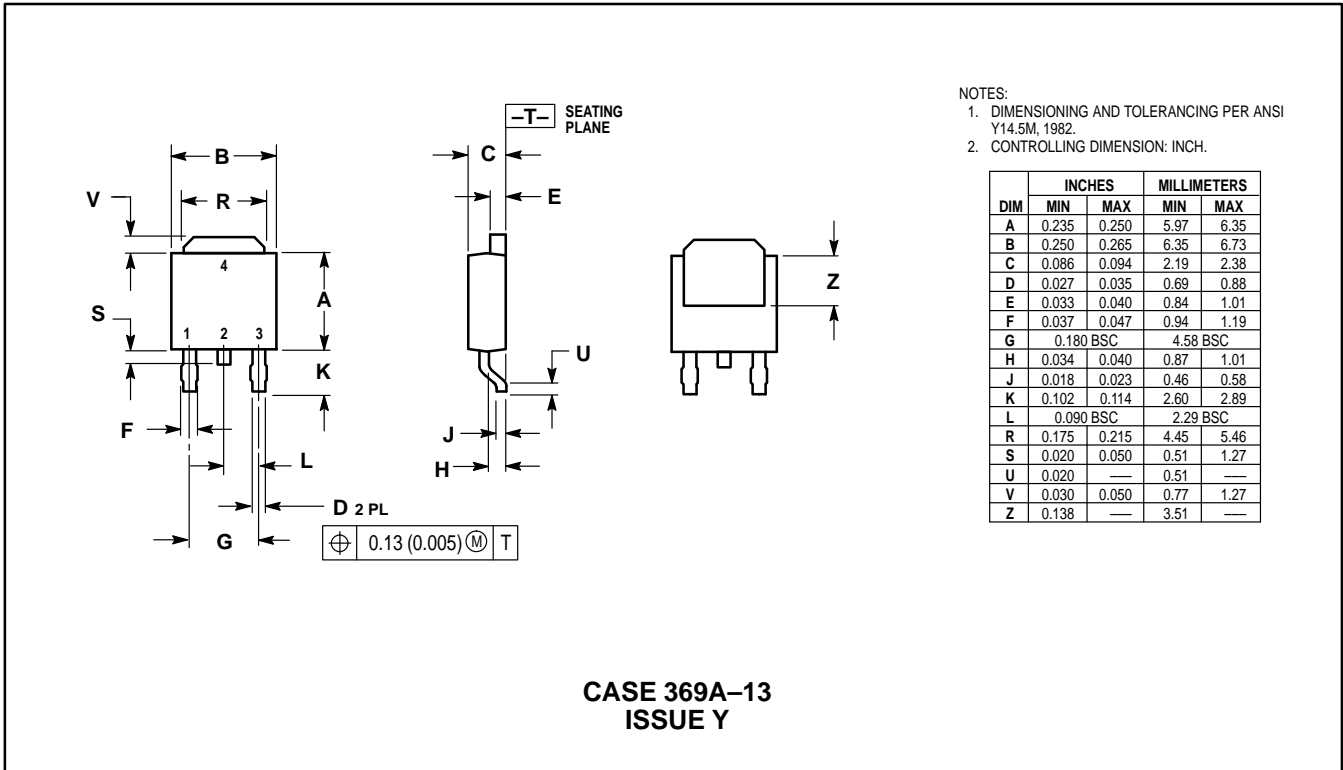



Figure 10. Thermal Response Junction to Ambient (Per Leg)

PACKAGE DIMENSIONS



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